

# THERE AND BACK AGAIN: A CIRCUIT EXTRACTION TALE

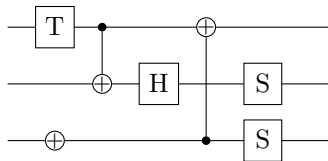
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Quantum Week of Fun — September 23, 2020

# Two paradigms of quantum computation

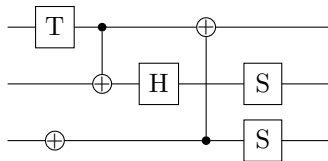
# Two paradigms of quantum computation

## Gate based



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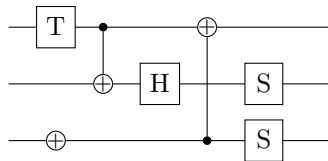
## Gate based



- ▶ Start with qubits in simple state
- ▶ Successively apply small unitaries
- ▶ Measure qubits at the end

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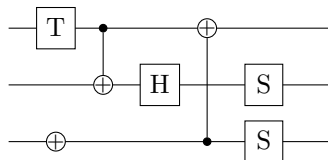


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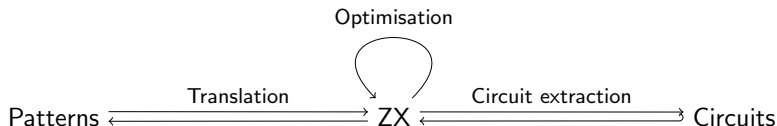


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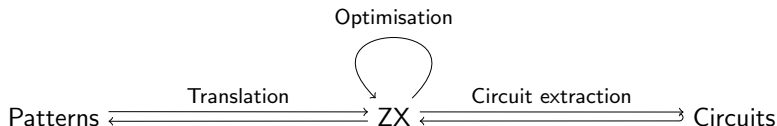
- ▶ Prepare some highly entangled resource state
- ▶ Perform successive measurements
- ▶ Later measurement choices depend on earlier outcomes

# Goal of this work



- ▶ Moving between gate based and measurement based models.
- ▶ ZX-calculus as a tool for translation.

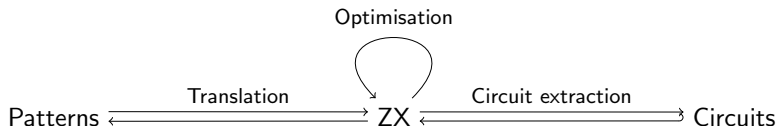
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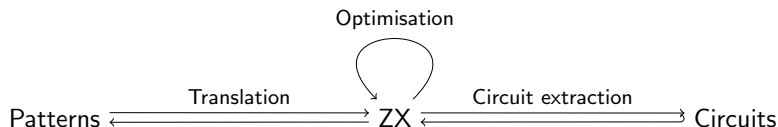


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- ▶ Optimise the number of qubits in measurement pattern.

# ZX-diagrams

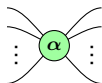
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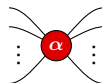
Z-spider

$$|0 \dots 0\rangle \langle 0 \dots 0| \\ + e^{i\alpha} |1 \dots 1\rangle \langle 1 \dots 1|$$



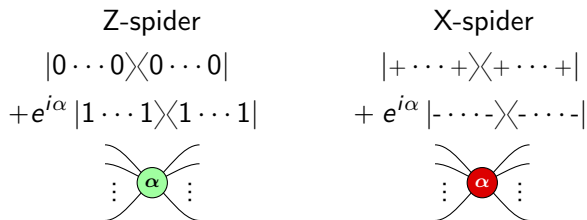
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$$|+\dots+\rangle \langle +\dots+| \\ + e^{i\alpha} |-\dots-\rangle \langle -\dots-|$$

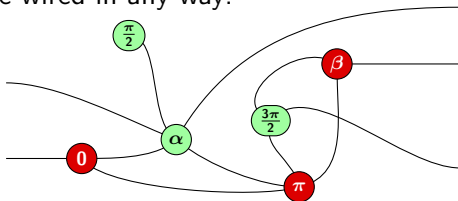


# ZX-diagrams

What gates are to circuits, *spiders* are to ZX-diagrams.



Spiders can be wired in any way:



# Quantum gates as ZX-diagrams

Every quantum gate can be written as a ZX-diagram:

$$S = \text{---} \textcircled{\frac{\pi}{2}} \text{---} \quad T = \text{---} \textcircled{\frac{\pi}{4}} \text{---}$$

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$$\text{CNOT} = \begin{array}{c} \text{---} \textcircled{\frac{\pi}{2}} \text{---} \\ | \\ \text{---} \textcircled{\frac{\pi}{2}} \text{---} \end{array} \quad \text{CZ} = \begin{array}{c} \text{---} \textcircled{\frac{\pi}{2}} \text{---} \\ | \\ \text{---} \square \text{---} \\ | \\ \text{---} \textcircled{\frac{\pi}{2}} \text{---} \end{array} = \begin{array}{c} \text{---} \textcircled{\frac{\pi}{2}} \text{---} \\ | \\ \text{---} \textcircled{\frac{\pi}{2}} \text{---} \end{array}$$

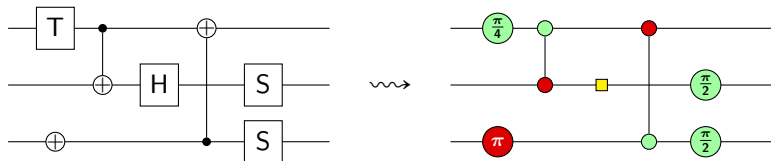
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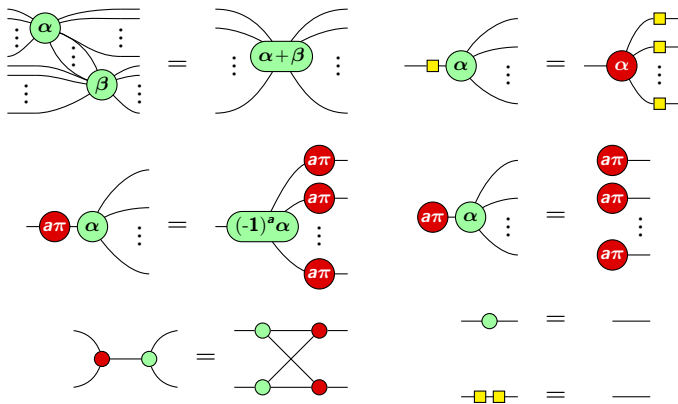
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# Rules for ZX-diagrams: The ZX-calculus



$$\alpha, \beta \in [0, 2\pi], a \in \{0, 1\}$$



# Universality and completeness

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Any linear map between qubits can be represented as a ZX-diagram.

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Any linear map between qubits can be represented as a ZX-diagram.

## Theorem (Vilmart 2018)

If two ZX-diagrams represent the same linear map, then they can be transformed into one another using the previous rules (and one additional one).

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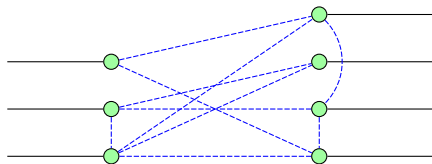
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We will translate each of these components into the ZX-calculus.

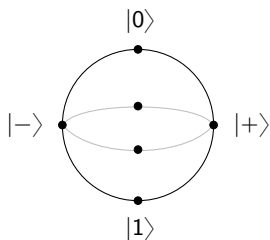


## (Open) graph states in the ZX-calculus

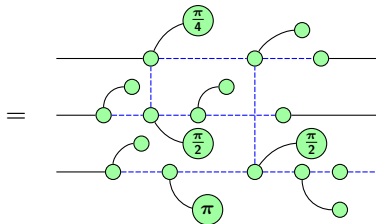
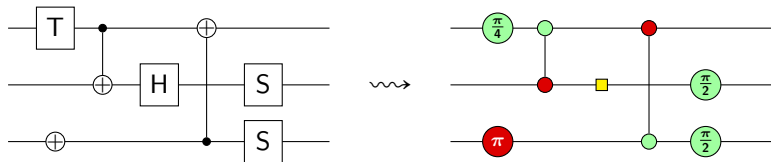


# Measurements in the ZX-calculus

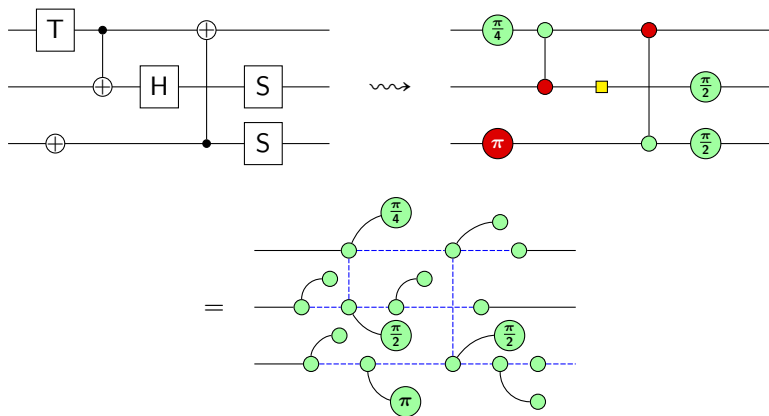
- ▶ XY-plane measurement: —  $\alpha + a\pi$
- ▶ XZ-plane measurement: —  $\frac{\pi}{2}$  —  $\alpha + a\pi$
- ▶ YZ-plane measurement: —  $\alpha + a\pi$



# Circuit to measurement pattern



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... but is this pattern deterministic?

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The previous translation of circuits to patterns always has causal flow.

# Main result

## Theorem

There is an efficient algorithm to transform a measurement pattern with gflow into a unitary quantum circuit (i.e. no ancillae required).

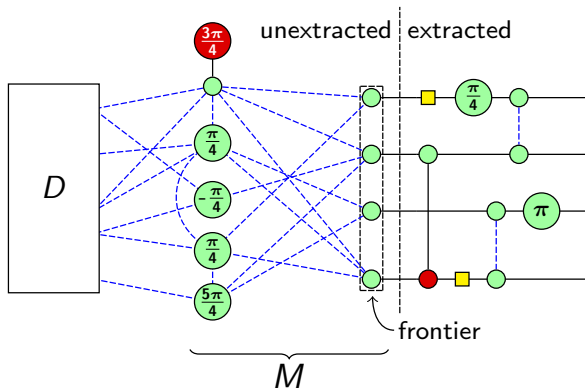
This extends previous work by Duncan, Perdrix, Kissinger and vdW that only deals with XY-plane measurements.

## General idea of the algorithm

- ▶ Translate measurement pattern to a ZX-diagram:

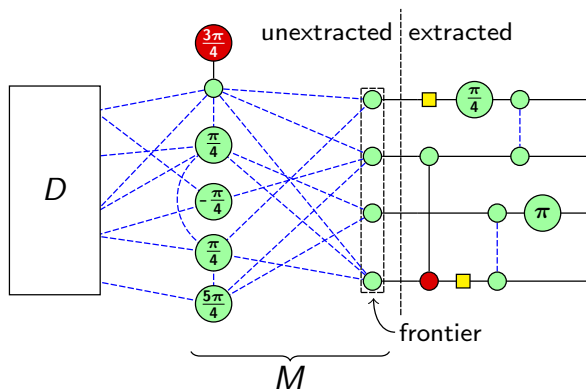
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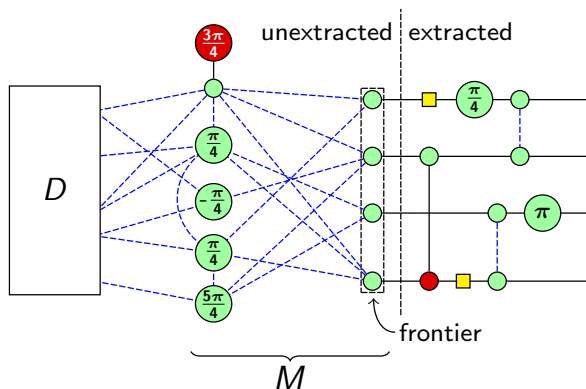
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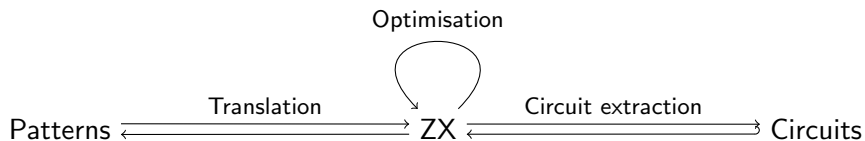
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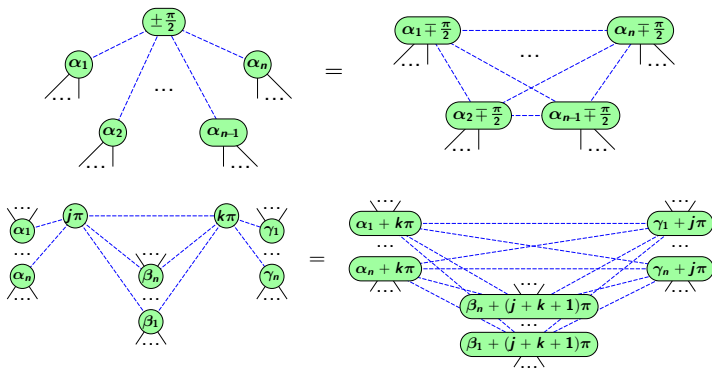
- ▶ Apply local rewrites to the unextracted part
- ▶ When the frontier vertices look like a circuit, move them to the extracted part

# The overview again



# Clifford vertex removal

Can remove any internal *Clifford* qubit from measurement pattern using *pivoting* and *local complementation*:



**Theorem:** These operations preserve existence of gflow.



# Circuits to measurement patterns

## Theorem

A  $n$ -qubit Clifford+T circuit with  $t$  T-gates can be efficiently transformed into a deterministic measurement pattern containing  $t + O(n)$  qubits.

# Conclusion

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- ▶ And back again (measurement patterns  $\rightarrow$  circuits)
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Thank you for your attention

Backens, Miller-Bakewell, de Felice, Lobski & vdW 2020,  
arXiv:2003.01664. *There and back again: A circuit extraction tale*