

PyZX: Large Scale Automated Diagrammatic Reasoning

Aleks Kissinger

`aleks@cs.ru.nl`

John van de Wetering

`john@vdwetering.name`

Institute for Computing and Information Sciences
Radboud University Nijmegen

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- ▶ Open source Python library
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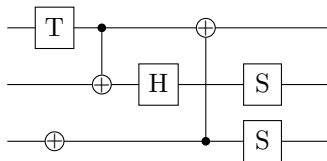
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- ▶ Why would we use ZX-diagrams for these things?

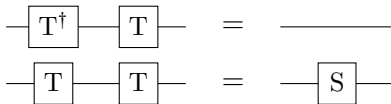
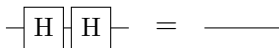
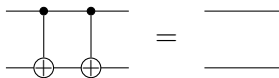
Circuit diagrams

$$\text{NOT} = \text{---}\oplus\text{---} \qquad \text{CNOT} = \begin{array}{c} \text{---}\bullet\text{---} \\ | \\ \text{---}\oplus\text{---} \end{array}$$

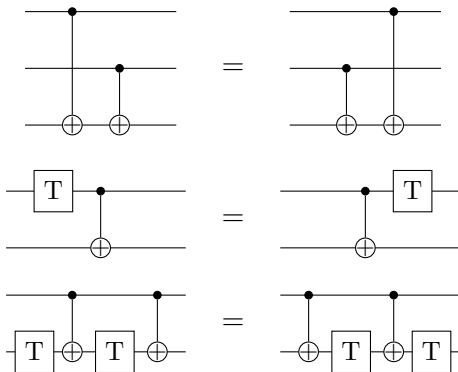
An example quantum circuit:



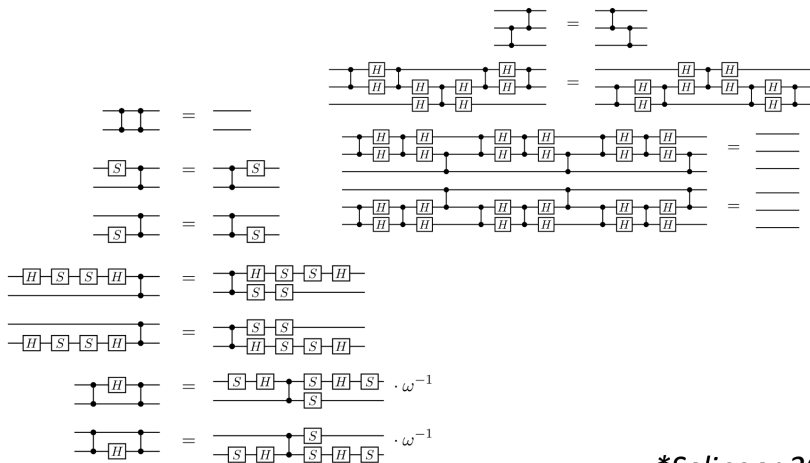
Circuit identities



Gate commutation

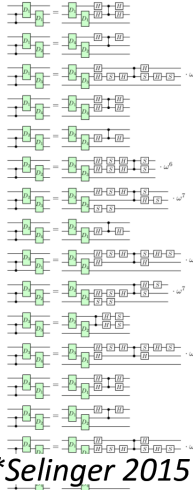
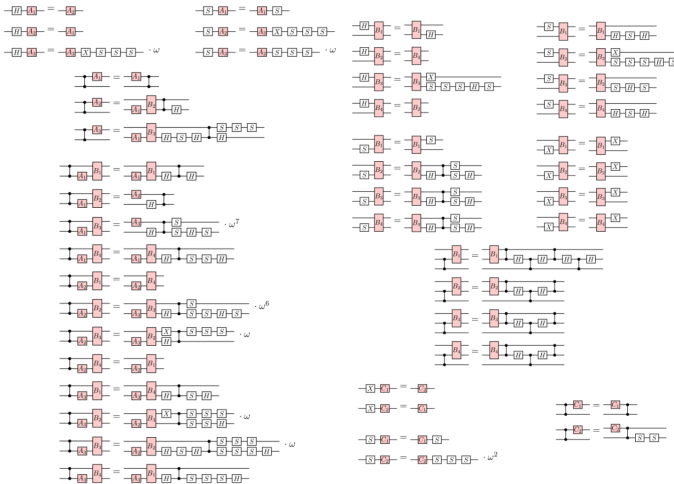


More circuit equalities



**Selinger 2015*

And more circuit equalities



**Selinger 2015*

And even more circuit equalities

$$R_1: \boxed{X^2} = \text{---}$$

$$R_2: \begin{array}{c} \text{---} \\ | \\ \boxed{X} \text{---} \boxed{X} \text{---} \boxed{X} \\ | \\ \text{---} \end{array} = \boxed{X}$$

$$R_3: \begin{array}{c} \boxed{X} \\ | \\ \boxed{X} \text{---} \boxed{X} \end{array} = \boxed{X}$$

$$R_4: \boxed{X^2} = \text{---}$$

$$R_5: \text{---} \times \text{---} = \begin{array}{c} \text{---} \\ | \\ \boxed{X} \text{---} \boxed{X} \text{---} \boxed{X} \\ | \\ \text{---} \end{array}$$

$$R_6: \begin{array}{c} \text{---} \\ | \\ \boxed{X} \end{array} = \begin{array}{c} \boxed{X} \\ | \\ \boxed{X} \text{---} \boxed{X} \end{array}$$

$$R_7: \boxed{T^4} = \text{---}$$

$$R_8: \begin{array}{c} \boxed{U^2} \\ | \\ \boxed{U^2} \end{array} = \begin{array}{c} \boxed{T^4} \\ | \\ \boxed{T^4} \end{array}$$

$$R_9: \begin{array}{c} \boxed{V^2} \\ | \\ \boxed{V^2} \end{array} = \begin{array}{c} \boxed{T^6} \boxed{U^2} \boxed{U^2} \\ | \\ \boxed{T^6} \boxed{U^2} \boxed{U^2} \end{array}$$

$$R_{10}: \omega^8 =$$

$$R_{11}: \boxed{X} \boxed{T} \boxed{X} = \omega \cdot \boxed{T^2}$$

$$R_{12}: \begin{array}{c} \boxed{T} \\ | \\ \boxed{X} \text{---} \boxed{X} \end{array} = \boxed{T}$$

$$R_{13}: \begin{array}{c} \boxed{X} \boxed{V} \boxed{X} \\ | \\ \boxed{V} \\ | \\ \boxed{V} \end{array} = \begin{array}{c} \boxed{T^2} \boxed{U^2} \boxed{U^2} \boxed{U^2} \\ | \\ \boxed{T^2} \boxed{U^2} \boxed{U^2} \boxed{U^2} \end{array}$$

$$\boxed{X} \boxed{T} = \omega \cdot \boxed{T^2} \boxed{X}$$

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$$\begin{array}{c} \boxed{X} \boxed{V} \\ | \\ \boxed{V} \end{array} = \begin{array}{c} \boxed{T^2} \boxed{U^2} \boxed{U^2} \boxed{U^2} \\ | \\ \boxed{T^2} \boxed{U^2} \boxed{U^2} \boxed{U^2} \end{array}$$

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*Amy, Chen, & Ross 2018

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because circuits are very rigid

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Enter ZX-diagrams

ZX-diagrams

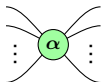
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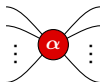
Z-spider

$$|0 \dots 0\rangle \langle 0 \dots 0| \\ + e^{i\alpha} |1 \dots 1\rangle \langle 1 \dots 1|$$



X-spider

$$|+\dots+\rangle \langle +\dots+| \\ + e^{i\alpha} |-\dots-\rangle \langle -\dots-|$$

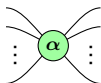


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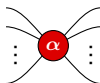
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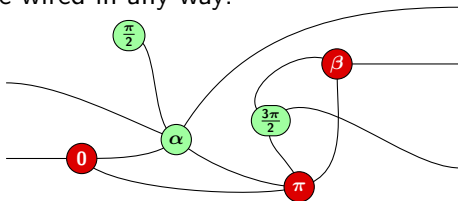


X-spider

$$|+\dots+\rangle \langle +\dots+| \\ + e^{i\alpha} |-\dots-\rangle \langle -\dots-|$$



Spiders can be wired in any way:



Quantum gates as ZX-diagrams

Every quantum gate can be written as a ZX-diagram:

$S = \text{---} \bigcirc \frac{\pi}{2} \text{---}$ $T = \text{---} \bigcirc \frac{\pi}{4} \text{---}$

$$H = \text{---} \square \text{---} := \text{---} \text{red circle } \frac{\pi}{2} \text{---} \text{green circle } \frac{\pi}{2} \text{---} \text{red circle } \frac{\pi}{2} \text{---}$$

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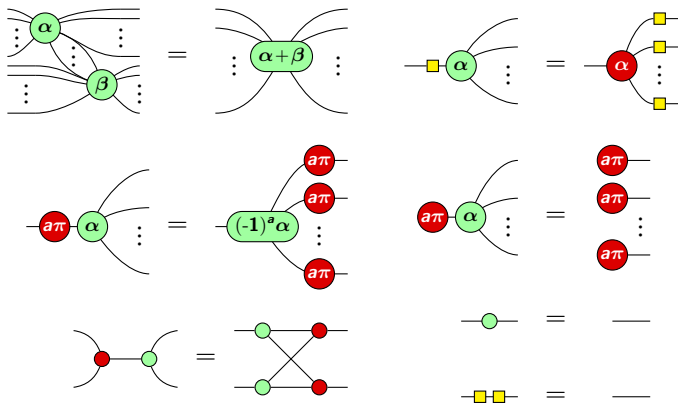
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CNOT =  CZ =  = 

Universality

Any linear map between qubits can be represented as a ZX-diagram.

Rules for ZX-diagrams: The ZX-calculus



$$\alpha, \beta \in [0, 2\pi], a \in \{0, 1\}$$

Completeness of the ZX-calculus

Theorem

If two ZX-diagrams represent the same linear map, then they can be transformed into one another using the previous rules (and some additional ones).

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So instead of dozens of circuit equalities, we just need a few simple rules.

Architecture of PyZX

Two main datastructures in PyZX: Circuits and Graphs.

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- ▶ Three types of vertices: *boundary*, X and Z .
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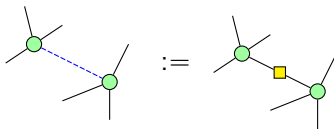
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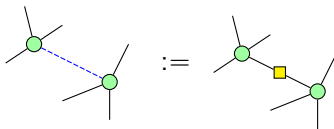
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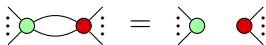
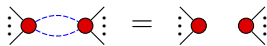
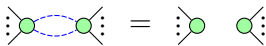
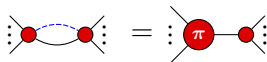
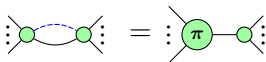
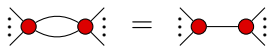
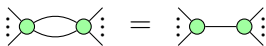
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- ▶ It is *undirected* and *simple*.

Dealing with parallel edges



Rewrite engine

Hierarchy of rewriting:

- ▶ First level: *parallel matcher* and *rewriter*.

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Important: simplifiers should be terminating.

Optimization using ZX-diagrams

Duncan, Kissinger, Perdrix, vdW 2019, arXiv:1902.03178

Graph-theoretic Simplification of Quantum Circuits with the ZX-calculus

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Goal: Minimize amount of T gates in circuit.

Demonstration time

Circuit	n	T	Best	Method	PyZX	PyZX +TODD
adder₈	24	399	213	RM_m	173	167
Adder8	23	266	56	NRSCM	56	56
Adder16	47	602	120	NRSCM	120	120
Adder32	95	1274	248	NRSCM	248	248
Adder64	191	2618	504	NRSCM	504	504
<i>csla-mux₃</i>	15	70	58	RM _r	62	45
<i>csum-mux₉</i>	30	196	76	RM _r	84	72
cycle17₃	35	4739	1944	RM_m	1797	1797
<i>gf(2⁴)-mult</i>	12	112	56	TODD	68	52
<i>gf(2⁵)-mult</i>	15	175	90	TODD	115	86
<i>gf(2⁶)-mult</i>	18	252	132	TODD	150	122
<i>gf(2⁷)-mult</i>	21	343	185	TODD	217	173
<i>gf(2⁸)-mult</i>	24	448	216	TODD	264	214
ham15-low	17	161	97	Tpar	97	97
ham15-med	17	574	230	Tpar	212	212
ham15-high	20	2457	1019	Tpar	1019	1013
hwb₆	7	105	75	Tpar	75	72
hwb₈	12	5887	3531	RM_{m&r}	3517	3501
<i>mod-mult-55</i>	9	49	28	TODD	35	20
<i>mod-red-21</i>	11	119	73	Tpar	73	73
mod5₄	5	28	16	Tpar	8	7
nth-prime₆	9	567	400	RM_{m&r}	279	279
<i>nth-prime₈</i>	12	6671	4045	RM _{m&r}	4047	3958
<i>qcla-adder₁₀</i>	36	589	162	Tpar	162	158
<i>qcla-com₇</i>	24	203	94	RM _m	95	91
<i>qcla-mod₇</i>	26	413	237	NRSCM	237	216
rc-adder ₆	14	77	47	RM _{m&r}	47	47
vbe-adder ₃	10	70	24	Tpar	24	24

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Using the latter, correctness was verified for all our circuits.

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Interesting challenges:

- ▶ Optimization using auxiliary qubits
- ▶ Compiling directly to lattice surgery procedure
(cf. previous talk)

Thank you for your attention!



github.com/Quantomatic/pyzx

zxcalculus.com/pyzx